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Last updated on May 5, 2026

Education

Ph.D. in Microelectronics, *Hong Kong University of Science and Technology (Guangzhou)* 2022 – present
Advisor: [Hongce Zhang](#)

M.S. in Electronic Engineering, *The Hong Kong University of Science and Technology*

B.S. in Electronic Information Technology, *Macau University of Science and Technology*

Experiences

Research Intern, *Li Auto Inc. (AI Infra)* 2026.03-2026.8

Visiting Scholar, *University of Maryland, College Park* (with [Prof. Cunxi Yu](#) on logic synthesis and ML for formal verification) 2024.09-2025.06

Independent Study and Research, *The Hong Kong University of Science and Technology* (with [Prof. Wei Zhang](#) on High-Radix Fast Fourier Transform for CNN Accelerator on FPGA using HLS) 2021.09-2022.02

Publications [[Google Scholar](#): 0k+ citations and an h-index of 1]

2026.....

1. *Avoiding Big Integers: Parallel Multimodular Algebraic Verification of Arithmetic Circuits*
[Clemens Hofstadler](#), [Daniela Kaufmann](#), and [Chen Chen](#)
IJCAR 2026
2. *TOPCELL: Topology Optimization of Standard Cell via LLMs*
[Zhan Song](#), [Yu-Tung Liu](#), [Chen Chen](#), [Guoheng Sun](#), [Jiaqi Yin](#), [Chia-tung Ho](#), [Ang Li](#), [Haoxing Ren](#), and [Cunxi Yu](#)
DAC 2026
3. *AutoPDR: Circuit-Aware Solver Configuration Prediction for Hardware Model Checking*
[Guangyu Hu](#), [Chen Chen](#), [Xiaofeng Zhou](#), [Jiaxi Zhang](#), and [Hongce Zhang](#)
ISED 2026
4. *ReVEAL: GNN-Guided Reverse Engineering for Formal Verification of Optimized Multipliers*
[Chen Chen](#), [Daniela Kaufmann](#), [Chenhui Deng](#), [Zhan Song](#), [Hongce Zhang](#), and [Cunxi Yu](#)
TACAS 2026
5. *eLogic: A E-Graph-based Logic Rewriting Framework for Majority-Inverter Graphs*
[Rongliang Fu](#), [Wei Xuan](#), [Shuo Yin](#), [Guangyu Hu](#), [Chen Chen](#), [Hongce Zhang](#), [Bei Yu](#), and [Tsung-Yi Ho](#)
DATE 2026

2025.....

6. *Revisit Choice Network for Synthesis and Technology Mapping*
[Chen Chen](#), [Jiaqi Yin](#), and [Cunxi Yu](#)
ICCAD 2025

7. *e-boost: Boosted E-Graph Extraction with Adaptive Heuristics and Exact Solving*
 Jiaqi Yin, Zhan Song, **Chen Chen**, Yaohui Cai, Zhiru Zhang, and Cunxi Yu
 ICCAD 2025
8. *ReVEAL: Reverse Engineering of Multiplier Architectures via Graph Learning for Computer Algebra Verification*
Chen Chen, Daniela Kaufmann, Chenhui Deng, Hongce Zhang, and Cunxi Yu
 IWLS 2025
9. *E-morphic: Scalable Equality Saturation for Structural Exploration in Logic Synthesis*
Chen Chen *, Guangyu Hu *, Cunxi Yu, Yuzhe Ma, and Hongce Zhang
 DAC 2025
10. *BoolE: Exact Symbolic Reasoning via Boolean Equality Saturation*
 Jiaqi Yin, Zhan Song, **Chen Chen**, Qihao Hu, and Cunxi Yu
 DAC 2025

2024.....

11. *E-Syn: E-Graph Rewriting with Technology-Aware Cost Functions for Logic Synthesis*
Chen Chen *, Guangyu Hu *, and Zuo Dongsheng, Cunxi Yu, Yuzhe Ma, and Hongce Zhang
 DAC 2024

Awards

Best Paper Nomination at ICCAD	2025
Best Paper Nomination at DAC	2025
3rd Place Award of EDathon Contest	2023

Service

Artifact Evaluation Committee.....

The 19th International Conference on Integrated Formal Methods (IFM2024)

The 20th International Conference on Integrated Formal Methods (IFM2025)

The 31th International Conference on Tools and Algorithms for the Construction and Analysis of Systems (TACAS2025)

Reviewer.....

ACM Transactions on Embedded Computing Systems (TECS)